WEST

Freeform Search g. No # 09/067, 599

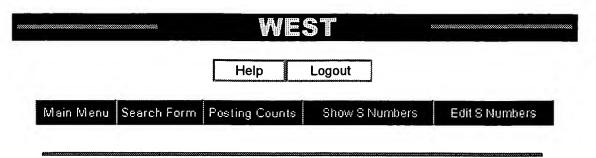
Database:	All Databases (USPT + EPAB + JPAB + DWPI + 1	ſDBD) ▼
Term:	and 133	△
Display 50	Documents in <u>Display Format: CIT</u> Hit List Hit Count Image	

Search	Clear	Help		Logout	
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Main Menu	Show S N	lumbers	Edit	S Number	s

Search History

DB Name	<u>Query</u>	Hit Count	Set Name
ALL	12 and (pattern near5 detect\$3 or pattern near5 match\$3)	202	<u>L33</u>
ALL	131 and (system near5 interfac\$3)	0	<u>L32</u>
ALL	130 and bit\$1 and pattern\$1	1	<u>L31</u>
ALL	129 and ((four or 4 or 128) near5 (byte\$1))	1	<u>L30</u>
ALL	127 and (mask\$3 near5 data)	3	<u>L29</u>
ALL	127 and 116	0	<u>L28</u>
ALL	125 and (stor\$3 or ram or rom or epram or eprom)	3	<u>L27</u>
ALL	125 and 11	0	<u>L26</u>
ALL	124 and 12	3	<u>L25</u>
ALL	110 and (mask\$3 near5 data)	16	<u>L24</u>
ALL	122 and (mask\$3 near5 data)	0	<u>L23</u>
ALL	11 and 12	4	<u>L22</u>
ALL	120 and 110	0	<u>L21</u>
ALL	12 and 116	18	<u>L20</u>
ALL	118 and 110	0	<u>L19</u>
ALL	111 and 116	4	<u>L18</u>

ALL	114 and 116	0	<u>L17</u>
ALL	(handshake near5 logic)	242	<u>L16</u>
ALL	(dasm or data accumulation state machine) same (handshake near5 logic)	0	<u>L15</u>
ALL	(dasm or data accumulation state machine)	15	<u>L14</u>
ALL	(mask near5 RAM or mask near5 random access memory) and (dasm or data accumulation state machine)	0	<u>L13</u>
ALL	(mask near5 RAM or mask near5 random access memory) same (dasm or data accumulation state machine)	0	<u>L12</u>
ALL	(mask near5 RAM or mask near5 random access memory)	619	<u>L11</u>
ALL	(pattern\$1 near5 match\$3 near5 logic\$1)	170	<u>L10</u>
ALL	(pattern\$1 near5 match\$3 near5 logic\$1).ti.	7	<u>L9</u>
ALL	(address near5 match\$3 near5 function\$1)	213	<u>L8</u>
ALL	l6 and l1	1	<u>L7</u>
ALL	12 and 15	313	<u>L6</u>
ALL	(ethernet near5 lan or ethernet near5 mac or ethernet near5 physical near5 layer near5 chip\$1)	1871	<u>L5</u>
ALL	(ethernet near5 interfac\$3 near5 card\$1)	311	<u>L4</u>
ALL	(ethernet near5 interfac\$3 near5 card\$1).ti.	6	<u>L3</u>
ALL	(network\$3 near5 interfac\$3) near5 (card\$1 or circuit\$1)	4023	<u>L2</u>
USPT	('4403303' '5311509' '5381414' '5396636' '5404544' '5412782' '5446914' '5485584' '5526355' '5560038' '5596575' '5604867' '5642360' '5659758' '5659794' '5689726')[PN]	16	<u>L1</u>



Search Results - Record(s) 1 through 4 of 4 returned.

Document ID: US 5689726 A

Entry 1 of 4

File: USPT

Nov 18, 1997

US-PAT-NO: 5689726

DOCUMENT-IDENTIFIER: US 5689726 A

TITLE: Computer system interface adapter capable of automatic self-configuration

and self-diagnosis before operating system initiation

DATE-ISSUED: November 18, 1997

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Lin; Shu-Chu

Hsinchu Hsien

N/A

N/A

TWX

US-CL-CURRENT: 710/10; 714/36

								,		
Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image

2. Document ID: US 5659794 A

Entry 2 of 4

File: USPT

Aug 19, 1997

US-PAT-NO: 5659794

DOCUMENT-IDENTIFIER: US 5659794 A

TITLE: System architecture for improved network input/output processing

DATE-ISSUED: August 19, 1997

INVENTOR - INFORMATION:

THE PROPERTY OF THE OWNER THE PROPERTY OF THE				
NAME	CITY	STATE	ZIP CODE	COUNTRY
Caldarale; Charles R.	Minneapolis	MN	N/A	N/A
Hancock; Peter J.	White Bear Lake	MN	N/A	N/A
Johnson; David R.	Oakdale	MN	N/A	N/A
Malek; Robert M.	White Bear Lake	MN	N/A	N/A
McBreen; James R.	Shoreview	MN	N/A	N/A
Mikkelsen; Hans C.	Afton	MN	N/A	N/A
Witalka; Jerome J.	Brooklyn Center	MN	N/A	N/A

US-CL-CURRENT: 710/1; 709/234, 710/129, 710/260, 710/5, 710/6

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KOMC	Image	ĺ
						_	_				

3. Document ID: US 5642360 A

Entry 3 of 4

File: USPT

Jun 24, 1997

DOCUMENT-IDENTIFIER: US 5642360 A

TITLE: System and method for improving network performance through inter frame

spacing adaptation

DATE-ISSUED: June 24, 1997

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Trainin; Solomon

Haifa 35708

N/A

N/A

ILX

US-CL-CURRENT: <u>370/230</u>; <u>370/229</u>, <u>370/232</u>, <u>370/252</u>, <u>370/465</u>, <u>709/235</u>

Full Title Citation Front Review Classification Date Reference Claims KMC Image

Document ID: US 5446914 A

Entry 4 of 4

File: USPT

Aug 29, 1995

US-PAT-NO: 5446914

DOCUMENT-IDENTIFIER: US 5446914 A

TITLE: Twisted pair and attachment unit interface (AUI) coding and transceiving

circuit with full duplex, testing, and isolation modes

DATE-ISSUED: August 29, 1995

INVENTOR-INFORMATION:

CITY STATE ZIP CODE COUNTRY NAME Paul; Prasun K. Santa Clara CA N/A N/A De Souza; Edwin Cupertino CA N/A N/A

US-CL-CURRENT: 709/235; 364/221.4, 364/221.7, 364/221.8, 364/229.5, 364/230.5, $\frac{364}{231.4}$, $\frac{364}{231.7}$, $\frac{364}{265.1}$, $\frac{364}{265.2}$, $\frac{364}{266.1}$

Full Title Citation Front Review Classification Date Reference Claims KWC Image

Terms	Documents
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Search Results - Record(s) 1 through 18 of 18 returned.

1. Document ID: US 5963543 A

Entry 1 of 18 File: USPT Oct 5, 1999

US-PAT-NO: 5963543

DOCUMENT-IDENTIFIER: US 5963543 A

TITLE: Error detection and correction apparatus for an asynchronous transfer mode

(ATM) network device

DATE-ISSUED: October 5, 1999

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Rostoker; Michael D. Boulder Creek CA N/A N/A Stelliga; D. Tony Pleasanton CA N/A N/A

US-CL-CURRENT: 370/232; 370/233, 370/395

Full Title Citation Front Review Classification Date Reference Claims KWC Image

2. Document ID: US 5931926 A

Entry 2 of 18 File: USPT Aug 3, 1999

US-PAT-NO: 5931926

DOCUMENT-IDENTIFIER: US 5931926 A

TITLE: Method and apparatus for dynamically calculating degrees of fullness of a

synchronous FIFO

DATE-ISSUED: August 3, 1999

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY
Yeung; Louise Y. Redwood City CA N/A N/A
Cen; Ling Beaverton OR N/A N/A

US-CL-CURRENT: <u>710/52</u>

					***********		*********			
Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWIC	Image

3. Document ID: US 5925097 A

Entry 3 of 18 File: USPT Jul 20, 1999

DOCUMENT-IDENTIFIER: US 5925097 A

TITLE: Directly programmable distribution element

DATE-ISSUED: July 20, 1999

INVENTOR-INFORMATION:

CITY STATE ZIP CODE COUNTRY Gopinath; Bhaskarpillai Watchung ŊJ N/A N/A Kurshan; David Sea Bright ŊJ N/A N/A Miljanic; Zoran Highland Park ŊJ N/A N/A

US-CL-CURRENT: 709/200; 709/202, 709/205, 709/226, 709/238, 709/239, 709/248

Full Title Citation Front Review Classification Date Reference Claims RMC Image

4. Document ID: US 5838904 A

Entry 4 of 18 File: USPT

Nov 17, 1998

US-PAT-NO: 5838904

DOCUMENT-IDENTIFIER: US 5838904 A

TITLE: Random number generating apparatus for an interface unit of a carrier sense with multiple access and collision detect (CSMA/CD) ethernet data network

DATE-ISSUED: November 17, 1998

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Rostoker; Michael D. Boulder Creek CA N/A N/A Stelliga; D. Tony Pleasanton CA N/A N/A Andover MA N/A N/A Paolino; Dave N/A Engelse; Willem A. H. Townsend MA N/A

US-CL-CURRENT: 709/250; 709/230

Full Title Citation Front Review Classification Date Reference Claims KWC Image

5. Document ID: US 5832233 A

Entry 5 of 18 File: USPT

Nov 3, 1998

US-PAT-NO: 5832233

DOCUMENT-IDENTIFIER: US 5832233 A

TITLE: Network coupler for assembling data frames into datagrams using

intermediate-sized data parcels DATE-ISSUED: November 3, 1998

INVENTOR-INFORMATION:

ZIP CODE COUNTRY STATE CITY NAME GB2 N/A N/A Derbyshire Hall; Trevor GB2 Manchester N/A N/A Higgins; Brian GB6 N/A N/A Edinburgh Robertson; Iain Bruce

US-CL-CURRENT: 709/236; 370/476

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Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KAIC	Image
211	11112	W 100 100 11								

6. Document ID: US 5802287 A

Entry 6 of 18

File: USPT

Sep 1, 1998

DOCUMENT-IDENTIFIER: US 5802287 A

TITLE: Single chip universal protocol multi-function ATM network interface

DATE-ISSUED: September 1, 1998

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Rostoker; Michael D. Boulder Creek CA N/A N/A Stelliga; D. Tony Pleasanton CA N/A N/A Bergantino; Paul Arlington MA N/A N/A

US-CL-CURRENT: $\frac{709}{250}$; $\frac{370}{230}$, $\frac{370}{235}$, $\frac{370}{395}$, $\frac{709}{238}$

7. Document ID: US 5754764 A

Entry 7 of 18 File: USPT May 19, 1998

US-PAT-NO: 5754764

DOCUMENT-IDENTIFIER: US 5754764 A

TITLE: Combination of input output circuitry and local area network systems

DATE-ISSUED: May 19, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Davis; Timothy D.	Arlington	TX	N/A	N/A
Baker; Roman	Sunnyvale	CA	N/A	N/A
Daugherty; Dan E.	Burleson	TX	N/A	N/A
Michael; Martin S.	Los Gatos	CA	N/A	N/A
Masood; Ahmed	Austin	TX	N/A	N/A
Waterson; Kent Bruce	Everman	TX	N/A	N/A
Fung; Hon C.	Arlington	TX	N/A	N/A
Koether; Mark Douglas	Grand Prairie	TX	N/A	N/A
Johnson; J. Scott	Fort Worth	TX	N/A	N/A

US-CL-CURRENT: 709/200; 710/5, 710/56, 711/170, 711/202, 713/500

Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWAC	Image
-										

8. Document ID: US 5668809 A

Entry 8 of 18 File: USPT Sep 16, 1997

US-PAT-NO: 5668809

DOCUMENT-IDENTIFIER: US 5668809 A

TITLE: Single chip network hub with dynamic window filter

DATE-ISSUED: September 16, 1997

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Rostoker; Michael D. Boulder Creek CA N/A N/A Stelliga; D. Tony Pleasanton CA N/A N/A

US-CL-CURRENT: 370/392

Full Title Citation Front Review Classification Date Reference Claims KMC Image

9. Document ID: US 5654962 A

Entry 9 of 18 File: USPT Aug 5, 1997

US-PAT-NO: 5654962

DOCUMENT-IDENTIFIER: US 5654962 A

TITLE: Error detection and correction method for an asynchronous transfer mode

(ATM) network device

DATE-ISSUED: August 5, 1997

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Rostoker; Michael D. Boulder Creek CA N/A N/A

Stelliga; D. Tony

US-CL-CURRENT: 370/232; 370/233, 370/395

Pleasanton

CA N/A N/A

Full Title Citation Front Review Classification Date Reference Claims KWC Image

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10. Document ID: US 5640399 A

Entry 10 of 18 File: USPT Jun 17, 1997

US-PAT-NO: 5640399

DOCUMENT-IDENTIFIER: US 5640399 A

TITLE: Single chip network router

DATE-ISSUED: June 17, 1997

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Rostoker; Michael D. Boulder Creek CA N/A N/A

Stelliga; D. Tony

Pleasanton

CA N/A

N/A

US-CL-CURRENT: 370/392; 370/402, 370/466

Full Title Citation Front Review Classification Date Reference Claims KWIC Image

11. Document ID: US 5634004 A

Entry 11 of 18 File: USPT May 27, 1997

US-PAT-NO: 5634004

DOCUMENT-IDENTIFIER: US 5634004 A

TITLE: Directly programmable distribution element

DATE-ISSUED: May 27, 1997

INVENTOR-INFORMATION:

COUNTRY STATE ZIP CODE NAME CITY N/A N/A Gopinath; Bhaskarpillai Watchung NJ N/A N/A Sea Bright NJ Kurshan; David Highland Park NJ N/A N/A Miljanic; Zoran

. US-CL-CURRENT: 710/132; 711/1

Full Title Citation Front Review Classification Date Reference Claims KWC Image

12. Document ID: US 5625825 A

Entry 12 of 18 File: USPT Apr 29, 1997

DOCUMENT-IDENTIFIER: US 5625825 A

TITLE: Random number generating apparatus for an interface unit of a carrier sense

with multiple access and collision detect (CSMA/CD) ethernet data network

DATE-ISSUED: April 29, 1997

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Rostoker; Michael D.	Boulder Creek	CA	N/A	N/A
Stelliga; D. Tony	Pleasanton	CA	N/A	N/A
Paolino; Dave	Andover	MA	N/A	N/A
Engelse; Willem A. H.	Townsend	MA	N/A	N/A

US-CL-CURRENT: 710/242; 340/825.5, 364/927.96, 364/DIG2, 370/448

Full Title Citation Front Review Classification Date Reference Claims KMC Image

13. Document ID: US 5446726 A

Entry 13 of 18 File: USPT Aug 29, 1995

US-PAT-NO: 5446726

DOCUMENT-IDENTIFIER: US 5446726 A

TITLE: Error detection and correction apparatus for an asynchronous transfer mode

(ATM) network device

DATE-ISSUED: August 29, 1995

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY
Rostoker; Michael D. Boulder Creek CA N/A N/A
Stelliga; D. Tony Pleasanton CA N/A N/A

US-CL-CURRENT: 370/232; 370/253, 370/395

		T. Comments								
Full	Title	Citation	Front	Review	Classification	Date	Reference	Claims	KWWC	Image

14. Document ID: US 5428754 A

Entry 14 of 18 File: USPT Jun 27, 1995

US-PAT-NO: 5428754

DOCUMENT-IDENTIFIER: US 5428754 A

TITLE: Computer system with clock shared between processors executing separate

instruction streams

DATE-ISSUED: June 27, 1995

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Baldwin; David R. Weybridge N/A N/A GBX

US-CL-CURRENT: 712/220; 364/DIG1, 712/30

Full	Title Citation	Front	Review	Classification	Date	Reference	Claims	KWAC	Image

15. Document ID: US 5329630 A

Entry 15 of 18 File: USPT Jul 12, 1994

DOCUMENT-IDENTIFIER: US 5329630 A

TITLE: System and method using double-buffer preview mode

DATE-ISSUED: July 12, 1994

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Baldwin; David R.

Weybridge

N/A N/A

GBX

US-CL-CURRENT: 711/173; 364/228.1, 364/238.4, 364/242.6, 364/242.91, 364/244, 364/244.8, 364/245.5, 364/245.7, 364/284, 364/284.1, 364/DIG1, 711/149, 712/28

Full Title Citation Front Review Classification Date R	Reference	Claims	KWIC	Image
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16. Document ID: US 5056015 A

Entry 16 of 18

File: USPT

Oct 8, 1991

US-PAT-NO: 5056015

DOCUMENT-IDENTIFIER: US 5056015 A

TITLE: Architectures for serial or parallel loading of writable control store

DATE-ISSUED: October 8, 1991

INVENTOR-INFORMATION:

NAME CITY

Weybr

STATE ZIP CODE N/A N/A

COUNTRY

Baldwin; David R.

Weybridge Salwayash /A N/A /A N/A GBX

Wilson; Malcolm E. Trevett; Neil F.

Kingston-upon-Thames

N/A N/A GBX GBX

US-CL-CURRENT: 703/27

Full Title Citation Front Review Classification Date Reference Claims KWC Image

17. Document ID: US 5042000 A

Entry 17 of 18

File: USPT

N/A

US-PAT-NO: 5042000

DOCUMENT-IDENTIFIER: US 5042000 A

TITLE: Integral transform method

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

COUNTRY

Baldwin; David R.

Weybridge

N/A

N/A

GBX

US-CL-CURRENT: 708/404

Full Title Citation Front Review Classification Date Reference Claims KWC Image

18. Document ID: US 4658250 A

Entry 18 of 18

File: USPT

Apr 14, 1987



DOCUMENT-IDENTIFIER: US 4658250 A

TITLE: Computer network operating method and apparatus for detecting and

exploiting data collisions to merge read transactions

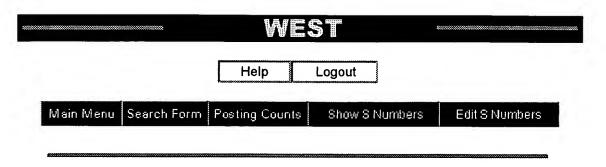
DATE-ISSUED: April 14, 1987

INVENTOR-INFORMATION:

NAME CITY STATE ZIP CODE COUNTRY Nering; Douglas G. Coolidge AZ 85228 N/A Bucanek; James L. Scottsdale AZ 85261 N/A

US-CL-CURRENT: 340/825.5; 370/462

Terms	Documents
12 and 116	18
Display 50 Documents	including document number 18
Display Format:	CIT Change Format



Search Results - Record(s) 1 through 1 of 1 returned.

L. Document ID: US 5537549 A

Entry 1 of 1

File: USPT

Jul 16, 1996

US-PAT-NO: 5537549

DOCUMENT-IDENTIFIER: US 5537549 A

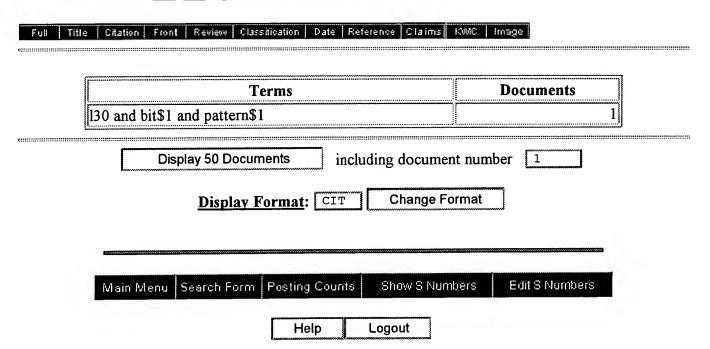
TITLE: Communication network with time coordinated station activity by time slot

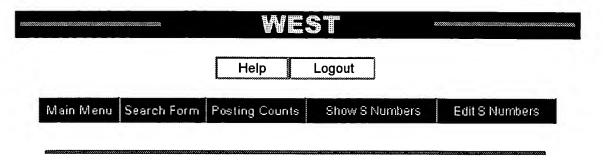
and periodic interval number DATE-ISSUED: July 16, 1996

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Gee; David J.	Washtenaw County	MI	N/A	N/A
Lucak; Mark A.	Washtenaw County	MI	N/A	N/A
Engdahl; Jonathan R.	Washtenaw County	MI	N/A	N/A
Siorek; Timothy	Jackson County	MI	N/A	N/A

US-CL-CURRENT: 709/224; 370/442, 370/503, 709/225, 710/45





Search Results - Record(s) 1 through 7 of 7 returned.

1. Document ID: US 5550528 A

Entry 1 of 7

File: USPT

Aug 27, 1996

US-PAT-NO: 5550528

DOCUMENT-IDENTIFIER: US 5550528 A

TITLE: Matching of digital patterns using analog logic

DATE-ISSUED: August 27, 1996

INVENTOR-INFORMATION:

NAME CITY

STATE ZIP CODE COUNTRY

Offord; Glen E. Ma

Macungie PA N/A N/A

Sonntag; Jeffrey L. Ro

Rockland Township, Berks County

PA N/A

N/A

US-CL-CURRENT: 340/146.2

Full Title Citation Front Review Classification Date Reference Claims KMC Image

2. Document ID: US 5530895 A

Entry 2 of 7

File: USPT

Jun 25, 1996

US-PAT-NO: 5530895

DOCUMENT-IDENTIFIER: US 5530895 A

TITLE: System and method for computer interface board identification by serially comparing identification address bits and asserting complementary <u>logic patterns</u>

for each match

DATE-ISSUED: June 25, 1996

INVENTOR-INFORMATION:

NAME

CITY

STATE

ZIP CODE

N/A

COUNTRY

Enstrom; Mark R.

Redmond

WA

N/A

US-CL-CURRENT: 710/9; 364/280.1, 364/927.93, 364/929.4, 364/DIG.1, 364/DIG.2,

<u>710/104</u>

Full Title Citation Front Review Classification Date Reference Claims KWC Image

3. Document ID: JP 08305780 A

Entry 3 of 7

File: JPAB

Nov 22, 1996

PUB-NO: JP408305780A

DOCUMENT-IDENTIFIER: JP 08305780 A

TITLE: MATCHING FOR DIGITAL PATTERN USING ANALOG LOGIC

PUBN-DATE: November 22, 1996

INVENTOR - INFORMATION:

NAME

OFFORD, GLEN E

SONNTAG, JEFFREY L

INT-CL (IPC): G06G 7/14; H03K 19/20

Full Title Citation Front Review Classification Date Reference Claims KMC Clip Img Image

4. Document ID: US 5550528 A

Entry 4 of 7

File: EPAB

Aug 27, 1996

PUB-NO: US005550528A

DOCUMENT-IDENTIFIER: US 5550528 A

TITLE: Matching of digital patterns using analog logic

PUBN-DATE: August 27, 1996

INVENTOR-INFORMATION:

NAME

COUNTRY

OFFORD, GLEN E US SONNTAG, JEFFREY L US

INT-CL (IPC): G05B 1/00
EUR-CL (EPC): G06F007/02

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWC | Image

5. Document ID: US 5530895 A

Entry 5 of 7

File: EPAB

Jun 25, 1996

PUB-NO: US005530895A

DOCUMENT-IDENTIFIER: US 5530895 A

TITLE: System and method for computer interface board identification by serially comparing identification address bits and asserting complementary <u>logic patterns</u> for each <u>match</u>

PUBN-DATE: June 25, 1996

INVENTOR-INFORMATION:

NAME

COUNTRY

ENSTROM, MARK R US

INT-CL (IPC): G06F 9/00; G06F 11/00; G06F 11/28

EUR-CL (EPC): G06F013/374; G06F015/177

Full Title Citation Front Review Classification Date Reference Claims KWC Image

6. Document ID: JP 10090375 A

Entry 6 of 7

File: DWPI

Apr 10, 1998

DERWENT-ACC-NO: 1998-275937

DERWENT-WEEK: 199825

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TITLE: Test pattern generation method for logic circuit e.g. X-OR gate - involves closing searching of decision tree when currently obtained assembly rule of test pattern of signal line of logic circuit matches with assembly rule stored

beforehand in memory

PRIORITY-DATA:

1996JP-0247443

September 19, 1996

PATENT-FAMILY:

PUB-NO

PUB-DATE

LANGUAGE

PAGES

MAIN-IPC

JP 10090375 A

April 10, 1998

N/A

014

G01R031/3183

INT-CL (IPC): G01R 31/3183; G06F 11/22

Full Title Citation Front Review Classification Date Reference Claims KMC Image

Document ID: US 5513298 A

Entry 7 of 7

File: DWPI

Apr 30, 1996

DERWENT-ACC-NO: 1996-230210

DERWENT-WEEK: 199623

COPYRIGHT 2000 DERWENT INFORMATION LTD

TITLE: Instantaneous context switching system for speech recogniser - includes selectable pointer maps in memory for selectively changing relationship between words and phonemes between application contexts and pattern matching logics

INVENTOR: BRICKMAN, N F; KLEIN, A G ; STANFORD, V M

PRIORITY-DATA:

1992US-0947634

September 21, 1992

1995US-0443685

May 18, 1995

PATENT-FAMILY:

PUR-NO

PUB-DATE

LANGUAGE

PAGES

MAIN-IPC

US 5513298 A

April 30, 1996

N/A

011

G10L009/00

INT-CL (IPC): G10L 9/00

Full Title Citation Front Review Classification Date Reference Claims KMC Image

Documents Terms 7 (pattern\$1 near5 match\$3 near5 logic\$1).ti.

Display 50 Documents

including document number

Display Format: CIT

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